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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known	
Application Number	10/680,271
Filing Date	OCTOBER 7, 2003
First Named Inventor	DITTO, et al.
Art Unit	2825
Examiner Name	DO, THUAN V.
Attorney Docket Number	5853-436

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TD		US- 5,291,555	03-01-1994	CUOMO, et al.	
TD		US- 5,745,655	04-28-1998	CHUNG, et al.	
TD		US- 2004/0036636	02-26-2004	MAI, et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3]-Number[4]-Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T[6]

Examiner Signature		Date Considered	9-8-2005

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. [1] Applicant's unique citation designation number (optional). [2] See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [3] Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [5] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [6] Applicant is to place a check mark here if English language Translation is attached.

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Art Unit	2825
Examiner Name	Unassigned
Attorney Docket Number	5853-436

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
TD		MURALI et al., "Realization of the fundamental NOR gate using a chaotic circuit," Physical Review 68:1-5, 2003.	✔
TD		MURALI et al., "IMPLEMENTATION OF NOR GATE BY A CHAOTIC CHUA'S CIRCUIT," Unpublished.	✔
TD		MUNAKATA et al., "Chaos Computing: Implementation of Fundamental Logical Gates by Chaotic Elements," IEEE Transactions on Circuits and Systems-I: Fundamental Theory and Applications 49:1629-1633, 2002.	✔
TD		MURALI et al., "Experimental Chaos Computing," Submitted to IEEE Trans. on Circuits and Systems, in 2003.	✔

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